# Electrically detected-magnetic-resonance identifications of defects at 4H-SiC(000 $\overline{1}$ )/SiO<sub>2</sub> interfaces with wet oxidation

Cite as: Appl. Phys. Lett. **115**, 151602 (2019); https://doi.org/10.1063/1.5116170 Submitted: 22 June 2019 . Accepted: 20 September 2019 . Published Online: 10 October 2019

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### ABSTRACT

We present electrically detected-magnetic-resonance (EDMR) identification of major and minor interface defects at wet-oxidized 4H-SiC(0001)/SiO<sub>2</sub> interfaces for C-face 4H-SiC metal-oxide-semiconductor field-effect transistors. The major interface defects are identified as *c*-axial types of carbon-antisite-carbon-vacancy ( $C_{Si}V_C$ ) defects. Their positive (+1) charge state generates a spin-1/2 EDMR center named "C-face defects" and behaves as an interfacial hole trap. This center is responsible for the effective hydrogen passivation of the C face. We also identify a minor type of interface defect at this interface called "P8 centers," which appear as spin-1 centers. Judging from their similarity to the P7 centers (divacancies,  $V_{Si}V_C$ ) in SiC, they were assigned to be a sort of basal-type interfacial  $V_{Si}V_C$  defect. Since both the  $C_{Si}V_C$  and  $V_{Si}V_C$  defects are known as promising single photon sources (SPSs) in SiC, the wet oxidation of the C face will have good potential for developing SPSs embedded at SiC surfaces.

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4H-SiC metal-oxide-semiconductor field-effect transistors (MOSFETs) are promising high-power and ultra-low-energy-loss switching transistors for next-generation power electronics. The standard 4H-SiC MOSFETs are fabricated on the front side of 4H-SiC(0001) wafers (the "Si face") in association with the thermal oxidation of the Si face in dry O<sub>2</sub> ambience.<sup>1</sup> On the other hand, the back side of the same wafer, i.e., 4H-SiC(0001) surface or "C face," exhibits quite different behaviors. For the C face, wet oxidation in H<sub>2</sub>O ambience is extremely useful for fabricating high-quality MOS interfaces.<sup>2,3</sup> For instance, the field-effect mobility ( $\mu_{\rm FE}$ ) easily exceeds  $70\,\text{cm}^2\,\text{V}^{-1}\,\text{s}^{-1}$  in wet-oxidized C-face MOSFETs, which is much higher than the standard values  $(\leq 40 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1})$  in Si-face MOSFETs.<sup>4,5</sup> While the reason behind such a drastic improvement is still a mystery, it is commonly held that interface defects associated with the wet oxidation are closely related to this behavior.<sup>6</sup> The wet oxidation is also important for other useful SiC surfaces such as 4H-SiC( $\overline{1}100$ ) ("*m* face") and 4H-SiC( $11\overline{2}0$ ) ("*a* face"), as these surfaces exhibit a similar sensitivity to the wet oxidation.<sup>7</sup> To reveal interface defects related to the wet oxidation, we have so far applied electrically detected magnetic resonance (EDMR) spectroscopy to the wet-oxidized C face and found some characteristic interface defects

there,<sup>6,8</sup> including one major type called "C-face defect." However, their microscopic origins still remain unclear.

In this Letter, we identify the origin of the C-face defects on the basis of their hyperfine (HF) interaction due to a <sup>13</sup>C nuclear spin. The resolved <sup>13</sup>C HF interaction clearly indicates that the C-face defects belong to the family of positively charged carbon-antisite-carbonvacancy complexes,  $C_{Si}V_C(+)$ . This type of defect is known as a fundamental defect in p-type 4H-SiC. In the bulk, four types of bulk  $C_{Si}V_{C}(+)$  have been identified,<sup>9</sup> as summarized in Figs. 1(a) and 1(b). In contrast, the wet oxidation of the C face selectively generates *c*-axial types of  $C_{Si}V_C$  at the interface [Figs. 1(a) and 1(c)], which is believed to correlate with atomistic features of wet oxidation and the C face. Moreover, we here show the presence of other characteristic interface defects at the wet-oxidized C face, labeled "P8 centers." They are spin-1 ESR centers belonging to the family of divacancy defects ( $V_{\rm Si}V_{\rm C}$ , the *P6/P7* centers<sup>10</sup>). It is worth noting that both the  $C_{Si}V_C$  and  $V_{Si}V_C$ defects are known as promising single photon sources (SPSs) in SiC.<sup>11,12</sup> Thus, we can expect that the wet oxidation of the C face has potential for exploring SPSs embedded at SiC surfaces.

For EDMR measurements, *n*-channel lateral C-face 4H-SiC MOSFETs (gate length/width =  $2/200 \ \mu$ m) were prepared using a 4°-off

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**FIG. 1.** Atomic models for interface defects at the wet-oxidized 4H-SiC(0001)/SiO<sub>2</sub> interface and for bulk  $C_{Si}V_{C}(+)$  centers<sup>9</sup> and bulk  $V_{Si}V_{C}$  centers<sup>10</sup> in 4H-SiC. (a) In the upper part, bulk  $C_{Si}V_{C}$  and  $V_{Si}V_{C}$  defects (*hh*, *kk*, *hk*, and *kh* types) are displayed. In the lower part, interface defects at the C face are shown. Blue bonds express unpaired-electron orbitals in  $C_{Si}V_{C}$  and  $V_{Si}V_{C}$ , which are located on a  $C_{Si}$  atom and on three C atoms of the  $V_{Si}$  side, respectively. (b) Bulk *c*-axial  $C_{Si}V_{C}(+)$  centers (*HEI*9a and *HEI*9b centers).<sup>9</sup> (c) Interface *c*-axial  $C_{Si}V_{C}(+)$  centers (indistinguishable *hh* and *kk* types). Blue dotted lines represent the periodicity of their 2nd nearest neighbors.

*p*-epitaxial layer (net acceptor concentration  $\approx 5 \times 10^{15} \text{ cm}^{-3}$ ).<sup>6,13</sup> Wet oxidation at 1000 °C (oxide thickness = 60 nm) and H<sub>2</sub> postoxidation anneal (POA) at 1100 °C were applied to the MOSFETs. We have already reported that EDMR observations on the C-face defects were successful only in the wet oxidation of the C face.<sup>6,13</sup> In fact, the C-face defects were not observed in dry-oxidized C-face MOSFETs with the same epitaxial layer, the same implanted source/ drain/well regions, and the same thermal history.<sup>8</sup> We also confirmed that H<sub>2</sub> POA after the dry oxidation is not useful to create the C-face defects and the *P*8 centers. It is also sure that these centers are not implantation damages because they were not observable in SiC-MOSFETs with an implanted ( $10^{20} \text{ cm}^{-3}$ ) channel region.<sup>13</sup>

Their EDMR signals increased after  $\gamma$ -ray irradiation due to breaking of the hydrogen (H) passivation of the C-face defects.<sup>6</sup> In this work, we further tried to maximize their EDMR signals by applying moderate postoxidation annealing at 900 °C in N2 ambience, which remarkably damages the H passivation of C-face MOSFETs.<sup>4</sup> After the 900 °C anneal treatment, the maximum  $\mu_{\rm FE}$  of the MOSFETs decreased from 80 to  $28 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ . We then performed roomtemperature EDMR measurements on the annealed C-face MOSFETs by utilizing either the gate-controlled-diode (GCD) EDMR mode or bipolar-amplification-effect (BAE) EDMR mode.<sup>14</sup> We found that the C-face defects were detectable in both the modes, and the GCD EDMR mode gives us a better signal-to-noise ratio for them (instead, the BAE mode gave us larger current changes). In this mode, a forward current of 50-60 nA was driven from the drain/source region to the well region, under applying a gate bias ( $V_{\rm G}$ ) of -10 V. The negative  $V_{\rm G}$  was indispensable for increasing the EDMR signals of the C-face defects because they can be converted from ESR-inactive states (neutral states) into ESR-active states (+1 charge states) by

capturing holes.<sup>6</sup> The EDMR signals (current changes induced by ESR transitions) were excited by a microwave of 9.462 GHz and 200 mW and were amplified by a lock-in amplifier synchronized to a magnetic-field modulation at 1.56 kHz.

Figure 2(a) shows a typical EDMR spectrum of the C-face defects in the annealed C-face 4H-SiC MOSFET. The spectrum clearly shows a doublet HF splitting of 1.1 mT, which is a characteristic signature of the C-face defects.<sup>6,8</sup> Figure 2(c) examines angular dependences of the EDMR signals, which can be well fitted by three central solid lines. These lines were simulated by adopting an electron spin (*S*) of 1/2, a  $C_{3\nu}$  (*c*-axial) symmetry, the g tensor shown in Table I, and with/without an isotropic doublet HF splitting of 1.1 mT. All the above signatures are consistent with those of the C-face defects.<sup>6</sup>

Looking carefully at both tails of the EDMR signals [Fig. 2(b)], we find a weak doublet HF splitting due to a  $^{13}$ C isotope (I=1/2, natural abundance = 1.1%). Blue solid lines express simulated  $^{13}$ C HF doublets, which were made from two replicas of the experimental spectrum shown in Fig. 1(a). The relative intensity of the replicas was set to be 0.55% in the whole spectrum. By assuming appropriate smooth baselines [dashed blue lines in Fig. 2(b)], the simulated  $^{13}$ C HF doublets can excellently reproduce the tails of the EDMR signals. In Fig. 2(c), we analyzed the angular dependences of the  $^{13}$ C HF splitting (solid symbols). In the  $^{13}$ C HF doublets, we could resolve one side of additional HF doublets (open symbols) due to the 1.1-mT splitting. As shown in the figure, both the  $^{13}$ C HF and 1.1-mT doublets are well simulated by solid lines, which were calculated using a  $^{13}$ C HF tensor shown in Table I.

As shown in Table I, the determined <sup>13</sup>C HF tensor is similar to those of the known  $C_{si}V_{C}(+)$  centers (the *HEI*9a and *HEI*9b centers)



**FIG. 2.** (a) EDMR spectrum of C-face defects in annealed C-face 4H-SiC MOSFETs with wet oxidation (SiO<sub>2</sub> thickness = 60 nm). The magnetic field (**B**) was modulated by an amplitude of 0.25 mT and the spectrum was measured for **B**// [0001]. (b) A doublet <sup>13</sup>C HF structure of C-face defects. Blue lines indicate simulation of <sup>13</sup>C HF doublets (see details in the text). (c) Angular maps of C-face defects. Solid and open circles represent their resonant fields for without and with an isotropic 1.1-mT HF splitting, respectively. The magnetic field was rotated from  $-30^{\circ}$  to 120° where **B**//[0001] for 0° and **B**//[120] for 90°. Solid lines are simulated by using the g and <sup>13</sup>C HF tensors listed in Table I. There are other minor defects which revealed isotropic signals (see grayed symbols). They are assigned to annealing damages because they were absent before annealing.

**TABLE I.** Spin-Hamiltonian parameters of C-face defects at the 4H-SiC(0001)/SiO<sub>2</sub> interface and bulk  $C_{Si}V_C(+)$  centers in *p*-type 4H-SiC. The spin-Hamiltonian in this system is defined as  $H = \mu_B \mathbf{S} \cdot \mathbf{g} \cdot \mathbf{B} + \mathbf{S} \cdot \mathbf{A} \cdot \mathbf{I} - g_n \mu_n \mathbf{I} \cdot \mathbf{B}$ , <sup>15</sup> where  $\mu_B$  is the Bohr magnetion, **S** is an electron spin operator, **g** is a gyromagnetic-factor tensor, **B** is an external magnetic-field vector, **A** is a <sup>13</sup>C HF tensor, and the third term represents a nuclear-spin Zeeman energy for a <sup>13</sup>C nucleus. Both the **g** and **A** tensors are well described by axially symmetric tensors (e.g.,  $A_{zz} \equiv A_{||}$  and  $A_{xx} = A_{yy} \equiv A_{\perp}$ ) with respect to the *c* axis.

Center	Origin (charge)	Spin (symmetry)	g		<b>A</b> ( <sup>13</sup> C) (mT)	
			<i>g</i> //	$g_\perp$	A <sub>//</sub>	$A_{\perp}$
Wet-oxidized	l C-face 4H-SiC MOSFETs,	present and Ref. 6				
C-face	Interfacial	1/2	2.0018	2.0027	8.0	2.1
defect	$C_{Si}V_C(+)$	$(C_{3\nu})$				
<i>p</i> -type 4H-Si	C bulk, Ref. 9					
HEI9a	$C_{Si}V_{C}(+)$ kk site	1/2	2.00227	2.00408	8.25	2.27
HEI9b	$C_{Si}V_{C}(+)$ <i>hh</i> site	$(C_{3\nu})$	2.00195	2.00379	9.95	3.71

in *p*-type 4H-SiC;<sup>9</sup> specifically, we found good agreement between the present center and the *HEI*9a center. In these bulk  $C_{Si}V_C(+)$ , a main <sup>13</sup>C HF interaction is generated from a  $C_{Si}$  atom of  $C_{Si}V_C$  where an electron spin is localized on a strongly *p*-like orbital [blue bonds in Figs. 1(a) and 1(b)]. Moreover, comparing the C-face defects with the *HEI*9a/b centers, their electron spin (1/2), their symmetry ( $C_{3v}$ ), and their charge state (+1) are also consistent with each other. Therefore, we conclude that the C-face defects belong to the family of *HEI*9a/b centers, i.e., they are *c*-axial types of interface  $C_{Si}V_C(+)$  centers at the wet-oxidized C face, as illustrated in Figs. 1(a) and 1(c).

However, a few fundamental questions on the above model remain. The first is an apparent inconsistency in the g tensors between the C-face defects and the *HEI*9a/b centers (Table I). We attribute this inconsistency to a perturbation of the interface. In fact, we already reported such a perturbation on the g tensor for the cases of typical dopants (nitrogen donors<sup>16</sup> and phosphorous donors<sup>17</sup>) when they are incorporated into the close vicinity of the interface.

The second is why only one type of EDMR center was observable at the interface despite four types of ESR centers distinguishable in bulk 4H-SiC [Fig. 1(a)]. In the bulk, two *c*-axial types [*kk* and *hh* types, see Fig. 1(b)] are distinguishable as the *HEI*9a and *HEI*9b centers, respectively.<sup>9</sup> A distinguishable point between them is the periodicity of their 2nd nearest neighbors (NNs), which are indicated by the blue dotted lines in Fig. 1(b). For the *HEI*9a and *HEI*9b centers, each 2nd NN atom connects to either the same column or a next column, respectively. Such a difference results in the distinguishable ESR centers. In contrast, at the interface shown in Fig. 1(c), each 2nd NN atom should connect to the amorphous SiO<sub>2</sub> network. This constraint supposedly makes the *hh* and *kk* types indistinguishable at the interface.

The interface  $C_{Si}V_C$  defects reasonably account for the effective H passivation of the C face.<sup>2–4</sup> Our previous ESR study confirmed that the *HEI9*/10 centers ( $C_{Si}V_C$ ) are one of the most reactive ESR centers to the H passivation in *p*-type 4H-SiC.<sup>18</sup> An unpaired electron on the  $C_{Si}$  atom can be passivated in the form of C-H bonds, and such passivation can be removed at above 800 °C.<sup>18</sup> This temperature range is consistent with the degradation temperature observed for wet-oxidized C-face MOSFETs.<sup>4</sup>

In addition to the  $C_{Si}V_C$  defects, there is a minor but interesting ESR center at the wet-oxidized C face, which we call the *P*8 centers. Figure 3(a) shows EDMR signals of two *P*8 centers (*P*8a and *P*8b) that

Appl. Phys. Lett. **115**, 151602 (2019); doi: 10.1063/1.5116170 Published under license by AIP Publishing were only detectable in wet-oxidized C-face MOSFETs. These *P*8 centers exhibit a large zero-field splitting (fine splitting) due to a close pair of two electron spins (S = 1). From the angular-map analyses shown in Fig. 3(b), their fine-splitting constants (D and  $E^{10}$ ) are estimated to be D = 50 mT and D = 43 mT for *P*8a and *P*8b, respectively, and  $E \leq 1 \text{ mT}$  for both the centers. These parameters are quite similar to



**FIG. 3.** (a) EDMR spectrum of *P*8 centers at the wet-oxidized 4H-SiC(0001)/SiO<sub>2</sub> interface. This spectrum was measured for a forward current of 85 nA under  $V_G = -27$  V in wet-oxidized (nonannealed) C-face MOSFETs. A strong central line arises from C-face defects (see Fig. 2). At least two spin-1 ESR centers (*P*8a and *P*8b) are resolved in the figure. (b) Angular maps of *P*8 centers. Magnetic-field rotation was set to be the same manner as used in Fig. 2. Solid lines are simulated angular patterns (see the text). (c) Gate-bias dependence of *P*8 centers. (d) Energy-level calculations on spin-1 systems of *P*8a and *P*8b (red and green lines, respectively) for a magnetic-field angle of 55°.

those of the *P*7 centers in bulk 4H-SiC (D = 43-48 mT and E < 0.7 mT).<sup>10,19</sup> The *P*7 centers are basal-type divacancies ( $V_{\text{Si}}V_{\text{C}}$ ) configured by 70°-off from the *c* direction, as is drawn on the right-hand side of Fig. 1(a). Judging from the similarity between the *P*8 and *P*7 centers, we assign the *P*8 centers to be a sort of interfacial  $V_{\text{Si}}V_{\text{C}}$  defect in a basal configuration. The *P*8 centers became detectable under a strong negative gate bias [Fig. 3(c)]. This is due to the gate-bias control of *P*8's charge state, similar to the case of the C-face defects.<sup>6</sup> One notable difference between the *P*8 and *P*7 centers is seen in their symmetrical axes where each fine splitting has a maximum. For the *P*8 center, the symmetrical axis is tilted by about 55° from the *c* axis [Fig. 3(b)], which is deviated from 70° for the *P*7 centers<sup>10</sup> [see Fig. 1(a)]. The tilt angle of 55° implies that the *P*8 centers are slightly rotated from the original  $V_{\text{Si}}V_{\text{C}}$  centers, like shown in the right-hand bottom side of Fig. 1(a), possibly due to an influence of the interface.

Looking over the above results, one finds an interesting contrast: basal types of  $C_{si}V_C$  (kh and hk types) are missing, while c-axial types of  $V_{Si}V_C$  (kk and hh types) are absent at the interface [see the bottom of Fig. 1(a)]. Unfortunately, we do not have a clear answer to this question; however, we believe that this is strongly correlated with the wetoxidation mechanism of the C face. Some tentative reasons are given with the help of Fig. 1(a). As seen in the figure, the basal types of interface C<sub>Si</sub>V<sub>C</sub> place V<sub>C</sub> as well as surrounding Si atoms in the interface side, while the *c*-axial types place them in the bulk side. As a result, the basal types are expectedly much more reactive to O atoms than the c-axial types, which may promote the selective annihilation of the basal types. With the same analogy, we reasonably expect that the amount of P8 centers (the basal-types  $V_{Si}V_C$ ) should be much less than that of C-face defects (the *c*-axial types of  $C_{Si}V_C$ ). On the other hand, "missing" c-axial types of V<sub>Si</sub>V<sub>C</sub> [bottom of Fig. 1(a)] possess unpaired-electron orbitals in the interface side, which are directly exposed to the oxidants. As a result, the c-axial types of interface  $V_{\rm Si}V_{\rm C}$  may be difficult to survive.

Another interesting feature is that both interface defects are "vacancy types." Also for the Si face, the formation of interfacial Si vacancies (V<sub>Si</sub>) was reported via EDMR analyses.<sup>20</sup> It is worth mentioning that the bistability of  $C_{Si}V_C$  and  $V_{Si}$  exists in SiC.<sup>9,21</sup> In *p*-type 4H-SiC (note that the present channel region is p-type),  $C_{Si}V_C$  should be more stable than  $V_{Si}$ . We imagine that the oxidation of the C face may initiate the extraction of a Si atom from SiC, forming a  $V_{Si}$ -like structure because the oxidation requires a reaction between Si and O. Such a precursor may be subsequently stabilized into a C<sub>Si</sub>V<sub>C</sub> form. The details of such atomistic mechanisms will be revealed by theoretical calculations in the future. Here, we should emphasize that the formation of  $C_{Si}V_C$  and  $V_{Si}V_C$  can only be successful in cooperation with H atoms. When the C face is oxidized by dry O2, its interface generates  $sp^2$ -bonded C cluster defects,<sup>8</sup> instead of the C<sub>Si</sub>V<sub>C</sub> and V<sub>Si</sub>V<sub>C</sub> defects. We observed the isotropic HF splitting of 1.1 mT for  $C_{Si}V_{C}$ , suggesting the presence of a <sup>1</sup>H HF interaction.<sup>6</sup> Such a H atom possibly attaches to the  $V_{\rm C}$  side of  $C_{\rm Si}V_{\rm C}$ . Since this HF interaction was alive even after the 900  $^{\circ}\mathrm{C}$  anneal treatment, the survival of a Si-H bond in the  $V_{\mathrm{C}}$  side may occur. Another possible assignment for the 1.1-mT HF splitting may be <sup>29</sup>Si HF interactions of three Si atoms of  $V_{\rm C}$ . Both the models will be inspected in the future.

The identification of interface defects in this study rehighlights the importance of wet oxidation of the C-face, as both  $C_{Si}V_C$  and  $V_{Si}V_C$  are known as promising SPSs in SiC.<sup>11,12</sup> In fact, the first step in creating SPSs in SiC-MOSFETs has been recently achieved using wetoxidized C-face MOSFETs.<sup>22,23</sup> In Fig. 3(d), we found zero-field splittings of 1.40 GHz and 1.21 GHz for the P8a and P8b centers. Note that the P8 centers may be more suitable to SPSs rather than bulk  $V_{\rm Si}V_{\rm C}$ , because the P8 centers are easily observable at room temperature.

In summary, we have clarified the microscopic origins of major and minor interface defects at wet-oxidized 4H-SiC(0001)/SiO<sub>2</sub> interfaces in C-face 4H-SiC MOSFETs. The major defects, called C-face defects,<sup>6.8</sup> have been identified as *c*-axial types of interface  $C_{\rm Si}V_{\rm C}$ defects. We also found a minor type of interface defect (the *P*8 centers), exhibiting spin-1 ESR centers with a zero-field splitting of 1.2–1.4 GHz. They were ascribed to a sort of basal-type interfacial  $V_{\rm Si}V_{\rm C}$  defects. The selective formation of *c*-axial  $C_{\rm Si}V_{\rm C}$  and basal  $V_{\rm Si}V_{\rm C}$  at the interface is believed to strongly correlate with atomistic mechanisms of wet oxidation of the C-face, which were briefly discussed in conjunction with the interface structure.

This work was supported by the Council for Science, Technology and Innovation (CSTI), the Cross-ministerial Strategic Innovation Promotion Program (SIP), and "Next-generation power electronics" (funding agency: NEDO). This work was also partly supported by a Grant-in-Aid (Grant No. 17H02781) from the Ministry of Education, Culture, Sports, Science and Technology of Japan.

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